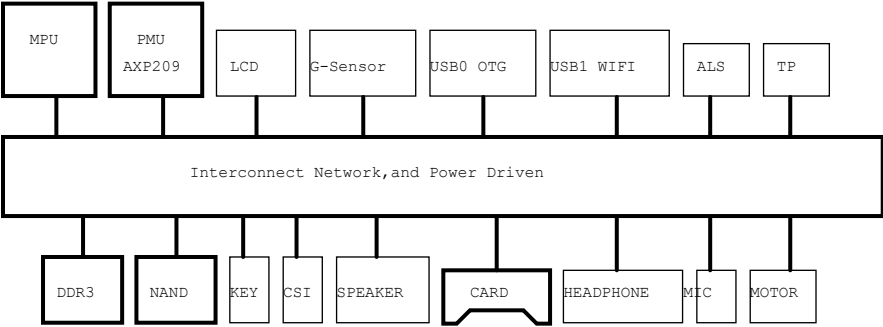


COVER

BLOCK



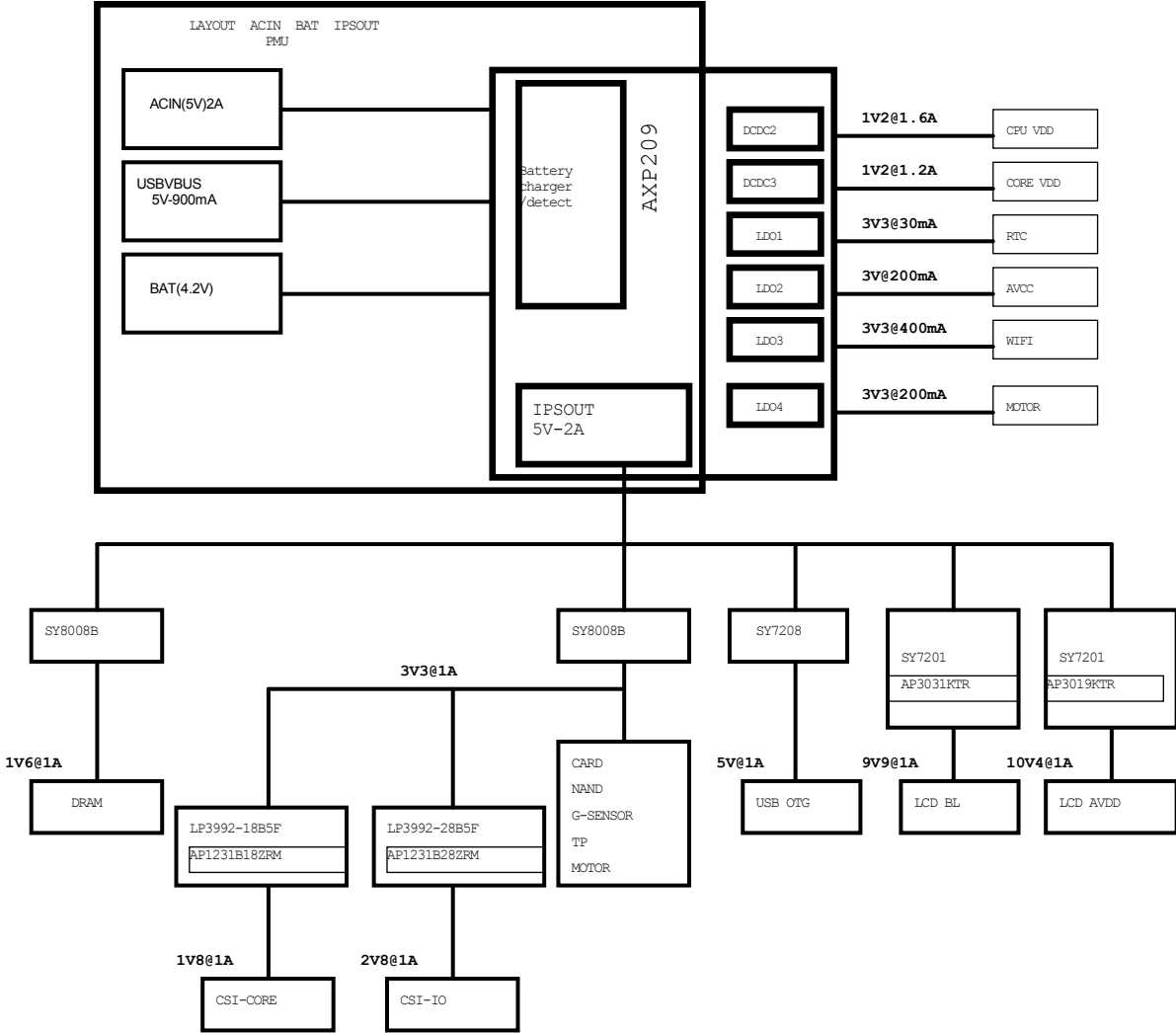
GPIO ASSIGNMENT

CFG	Define	Function
PB0	TWI0_SCK	TWI0
PB1	TWI0_SDA	
PB2	PWM0	LCD
PB3	GPIO-IN	SD0-DET-N
PB4	EINT	ALS-INT
PB10	GPIO-OUT	CSI-STY
PB15	TWI1_SCK	TWI1
PB16	TWI1_SDA	
PB17	TWI2_SCK	TWI2
PB18	TWI2_SDA	
PG0	GPIO-OUT	PA-SHDN
PG1	GPIO-OUT	MT-EN
PG2	GPIO-OUT	TP-WAKE-RST
PG3	UART-TX	UART-TX
PG4	UART-RX	UART-RX
PG9	EINT	TP-INT
PG10	GPIO-IN	USB0-IDDET
PG11	GPIO-IN	USB0-VBUSDET
PG12	GPIO-OUT	USB0-DRV
RM0	Define	Function
GPI00	GPIO-OUT	LCD-PWR
GPI01	GPIO-OUT	LCD-BL-EN
GPI02	GPIO-OUT	CSI-PWR-EN
GPI03	GPIO-OUT	CSI-RST

REVISION HISTORY

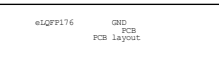
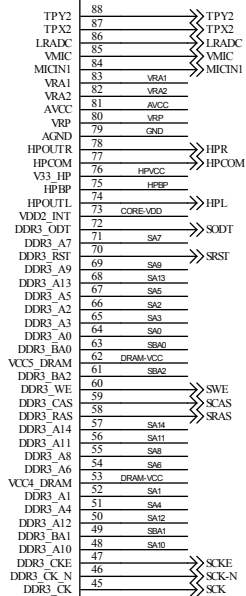
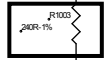
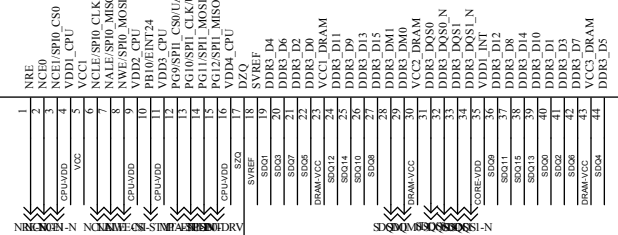
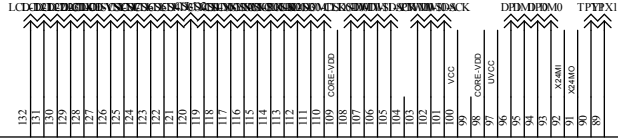
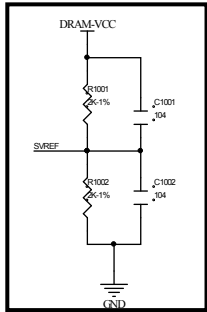
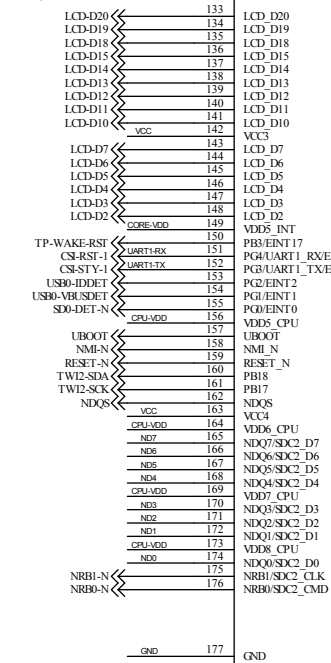
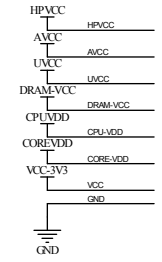
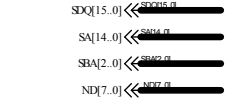
Revision	Description	Date	Drawn	Checked	Approved

POWER TREE

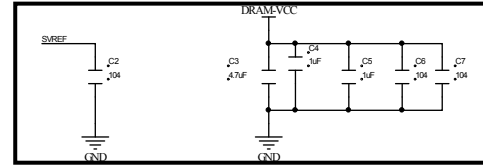


File	
Size	Document Number
A	COVER
Date	Rev
	7

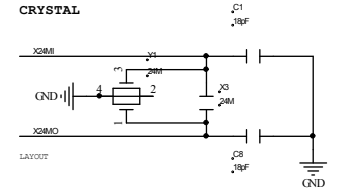
## CPU



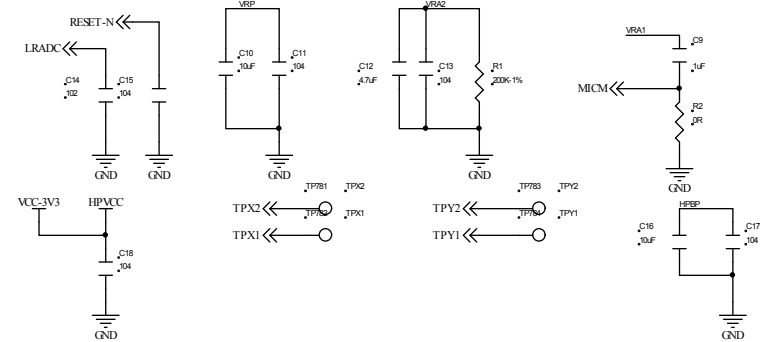
DRAM



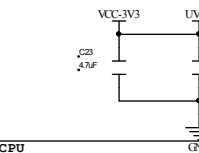
## CRYSTAL



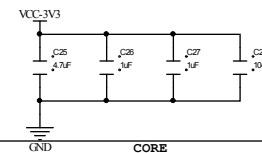
AUDIO-TP



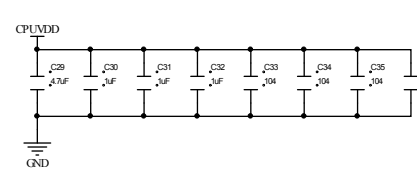
## USB



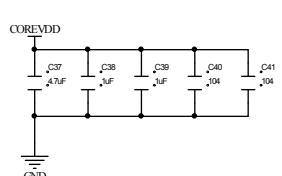
## GPIO



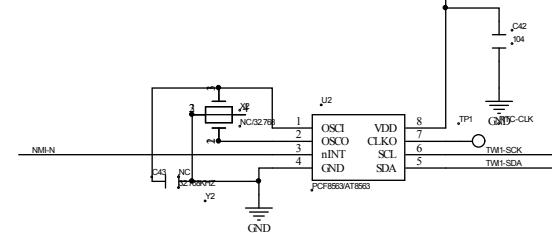
## CPU



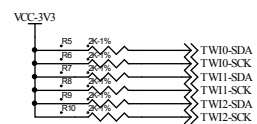
**CORE**



## RTC



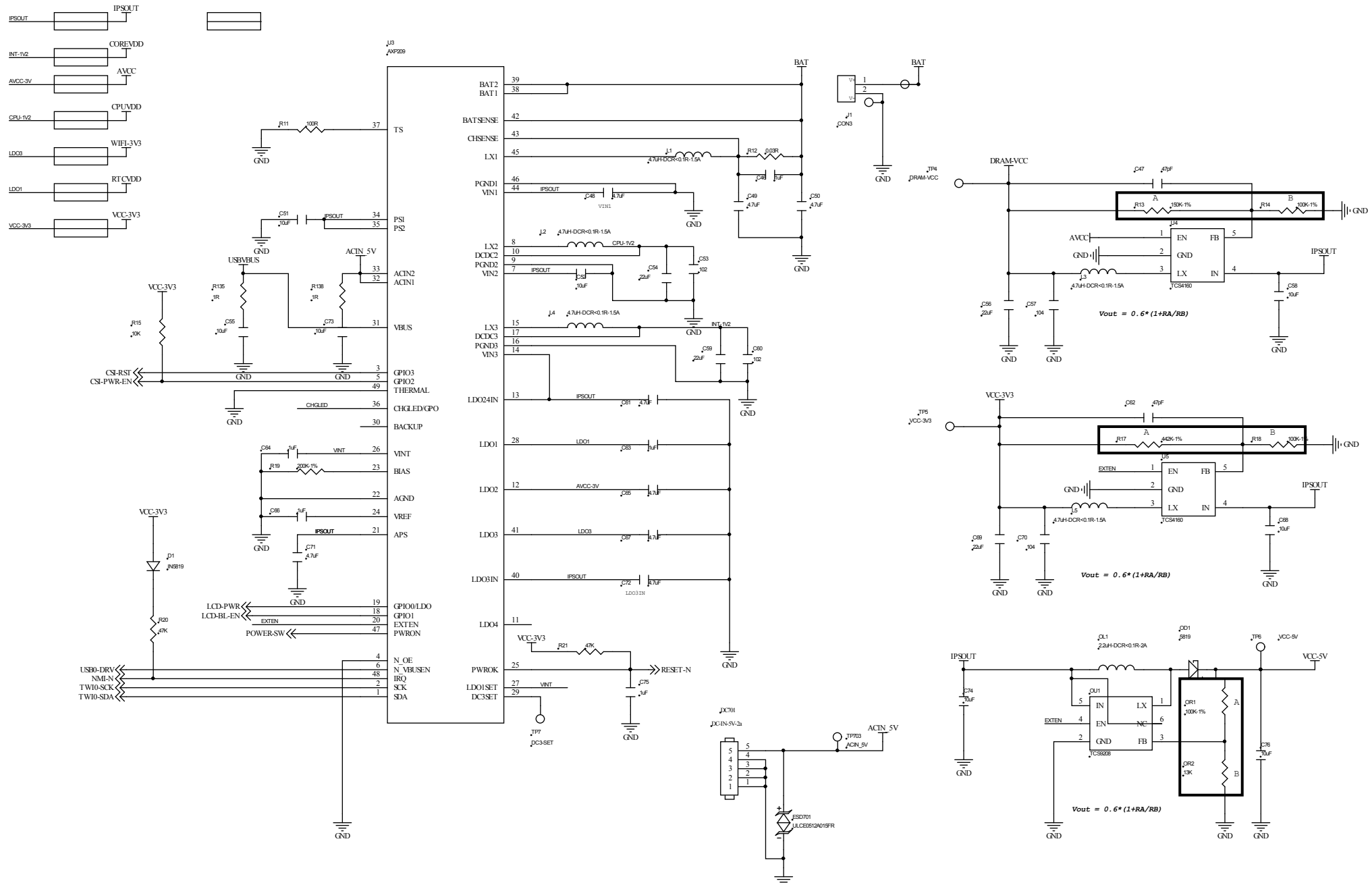
TWI-PULLUP



APF3_PWD_DDR3					
Size A	Document Number CPU				Rev ?
Date		Print	$\alpha$	$\beta$	$\gamma$

## POWER

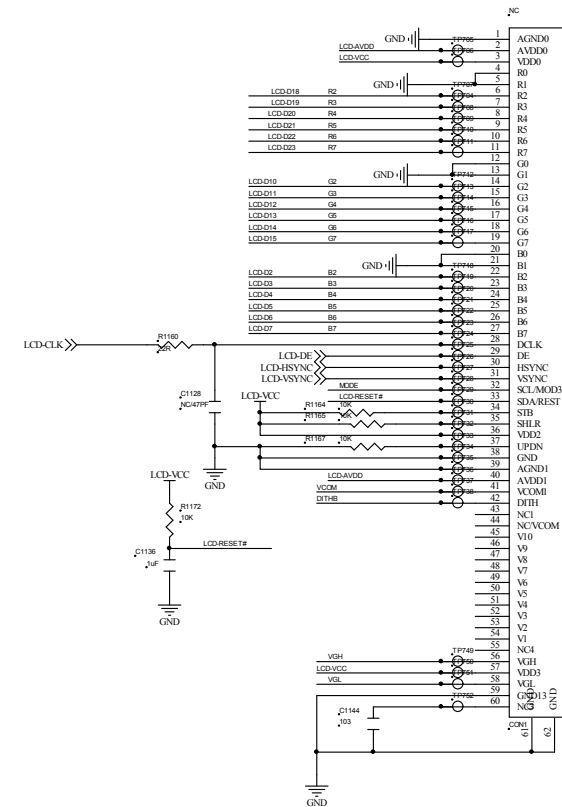
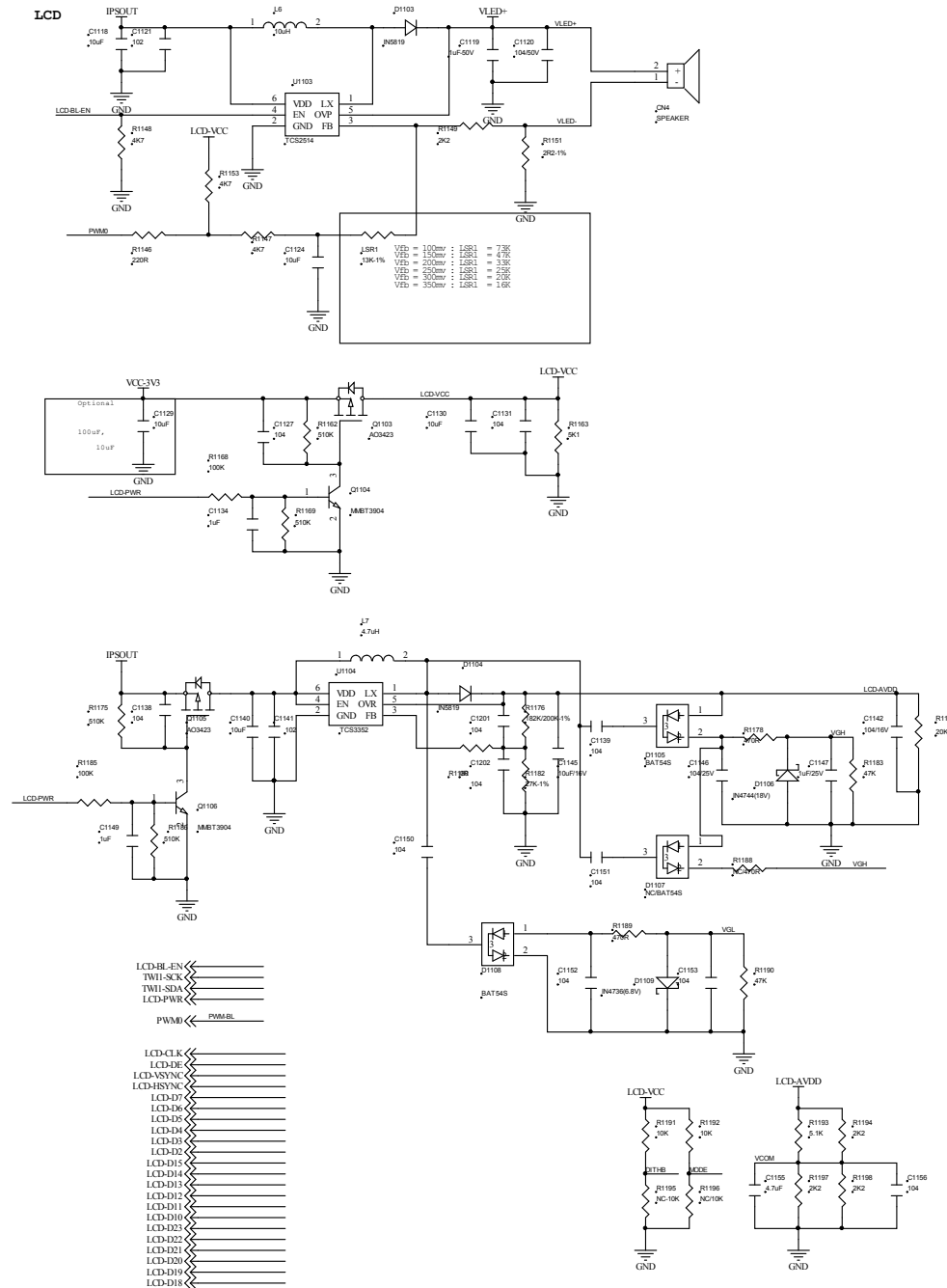
**POWER LINE:Width>=60mil**



Date		Sheet		of		Page	
------	--	-------	--	----	--	------	--

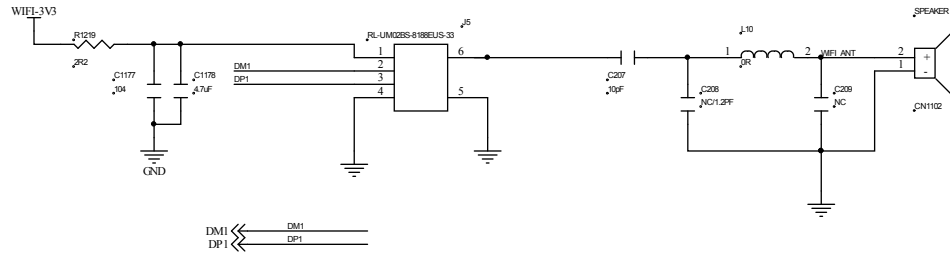


**LCD7"INNONUX**

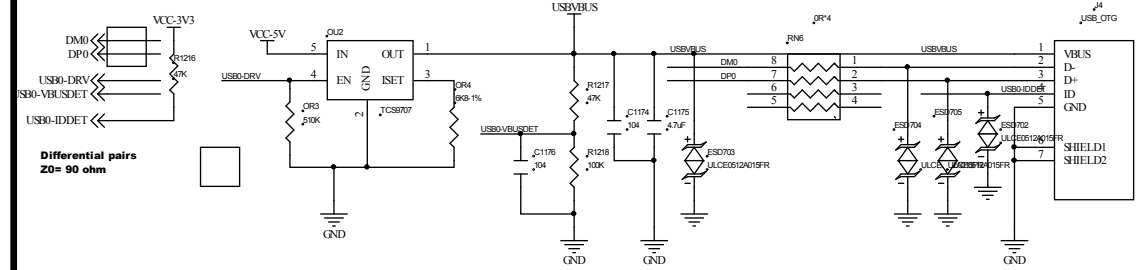




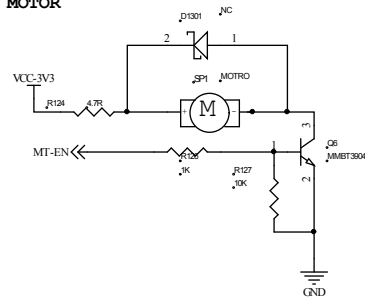
WIFI-8188



## USB-OTG



## MOTOR

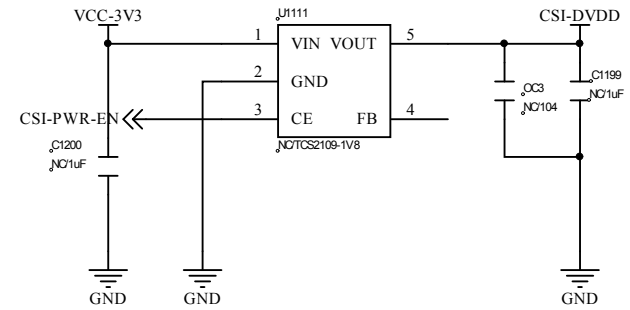
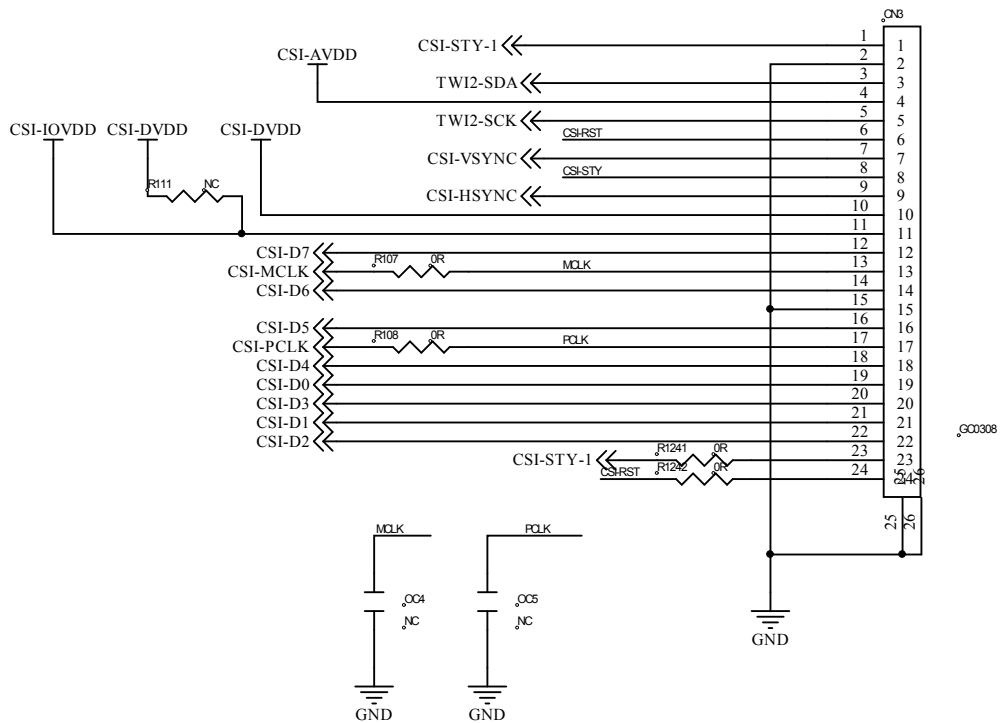
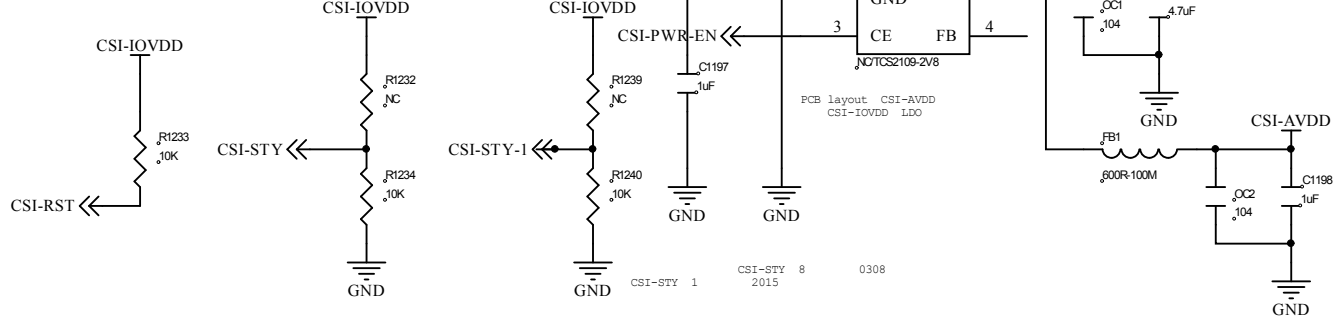


MAINCHIP\_PAD\_DDR3

Title	USB-CARD
-------	----------

Size A	Document Number • <V1.0>	Rev
Date	Sheet 2 of 8	

CSI



MAINCHIP\_PAD\_DDR3

Title			
Size	Document Number	Rev	
A	<V1.0>		
Date	Sheet	of	Rev



